TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	BIPOLAR JUNCTION TRANSISTOR AND FABRICATING METHOD
Application Number :	
Date :	
First Named Applicant	t: Anchor Chen
Confirmation Number:	
Attorney Docket Numb	per: NAUP0486USA3

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Documents being submitted:	Files
us-request	NAUP0486-3-usrequ.xml
	us-request.dtd
	us-request.xsl
us-fee-sheet	NAUP0486-3-usfees.xml
	us-fee-sheet.xsl
	us-fee-sheet.dtd
us-declaration	NAUP0486-3ASS1.tif
application-body	NAUP0486-3-trans.xml
	us-application-body.xsl
	application-body.dtd
	wipo.ent
	mathml2.dtd
	mathml2-qname-1.mod
	isoamsa.ent
	isoamsb.ent
	isoamsc.ent
	isoamsn.ent
	isoamso.ent
	isoamsr.ent
	isogrk3.ent
	isomfrk.ent
	isomopf.ent
	isomscr.ent
	isotech.ent
	isobox.ent
	isocyr1.ent
	isocyr2.ent
	isodia.ent
	isolat1.ent
	isolat2.ent
	isonum.ent
	isopub.ent
	mmlextra.ent
	mmlalias.ent
	soextblx.dtd
	Naup0486usa-01.TIF
	Naup0486usa-02.TIF
	Naup0486usa-03.TIF
	Naup0486usa-04.TIF
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	Naup0486usa-06.TIF
	Naup0486usa-07.TIF
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	Naup0486usa-09.TIF
	Naup0486usa-10.TIF
	Naup0486usa-11.TIF
	Naup0486usa-12.TIF
	Naup0486usa-13.tif
<u>-</u>	Naup0486usa-14.tif

us-declaration us-declaration	NAUP0486-3DEC1.tif NAUP0486-3DEC2.tif	
Comments		